

CLAIMS

What is claimed is:

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1 1. A method for multiple memory aliasing for a configurable system-on-a-
2 chip, comprising:
3 executing code from a read-only memory (ROM) internal memory, said
4 ROM internal memory having an alias;
5 locating a configuration program in the ROM internal memory;
6 disabling the ROM internal memory alias; and
7 jumping to a secondary initialization routine located in a FLASH
8 external memory.

1 2. The method of claim 1 further comprising:
2 configuring the system-on-a-chip using the secondary initialization
3 routine located in the FLASH external memory.

1 3. The method of claim 2 further comprising:
2 resetting a central processing unit of the system-on-a-chip.

1 4. The method of claim 3 further comprising:
2 executing code starting with the bottom of the ROM internal memory.

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1 5. The method of claim 1, wherein the FLASH external memory has an
2 external alias, the method further comprising:
3 selectively programming the alias of the ROM internal memory;
4 selectively programming the external alias of the FLASH external
5 memory; and
6 programming the priority of the alias and the external alias.

1 6. An apparatus for multiple memory aliasing for a configurable system-
2 on-a-chip, comprising:
3 means for executing code from an internal memory, said internal
4 memory having an alias;
5 means for locating a configuration program in the internal memory;
6 means for disabling the external memory alias; and
7 means for jumping to a secondary initialization routine.

1 7. The apparatus of claim 6 further comprising:
2 means for configuring the system-on-a-chip using the secondary
3 initialization routine.

1 8. The apparatus of claim 7 further comprising:

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means for resetting a central processing unit of the system-on-a-chip.

1 9. The apparatus of claim 8 further comprising:

2 means for executing code starting with the bottom of the internal
3 memory.

1 10. The apparatus of claim 9 further comprising:

2 means for setting up an application program for the system-on-a-chip
3 from the internal memory.

1 11. A computer readable medium having instructions which, when executed
2 by a processing system, cause the system to perform a method comprising:

3 executing code from an internal memory, said internal memory having
4 an alias;

5 locating a configuration program in the internal memory;

6 disabling the internal memory alias; and

7 jumping to a secondary initialization routine.

1 12. The medium of claim 11, wherein the executed instructions further cause
2 the system to:

3 configure the system-on-a-chip using the secondary initialization
4 routine.

1 13. The medium of claim 12, wherein the executed instructions further cause
2 the system to:
3 reset a central processing unit of the system-on-a-chip.

1 14. The medium of claim 13, wherein the executed instructions further cause
2 the system to:
3 execute code starting with the bottom of the internal memory.

1 15. The medium of claim 14, wherein the executed instructions further cause
2 the system to:
3 set up an application program for the system-on-a-chip from the internal
4 memory.

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